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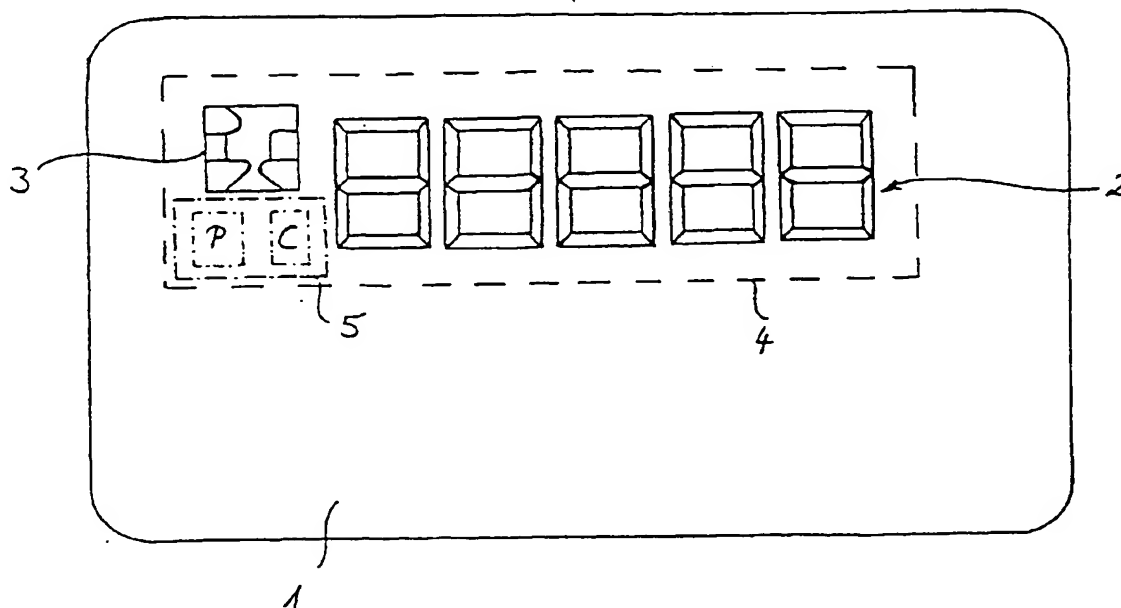
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[Fortsetzung auf der nächsten Seite]

(54) Title: CHIP CARD WITH A DISPLAY

(54) Bezeichnung: CHIPKARTE MIT DISPLAY



(57) Abstract: The chip card processor (P) and the display controller (C) of a display chip card (1) are embodied in a single chip (5). Said chip (5) is disposed on the same substrate (4) on which the display (2) is also arranged. As a result, there is no need for connection lines and contacts between the display controller and the processor chip. The display (2) and the chip (5) form a mechanical unit and the processor chip (P) no longer needs to be inserted separately. Preferably, the display substrate (4) extends, in conjunction with the processor chip (P) or at least strip conductors leading to the processor chip (P), underneath the contact field (3) of the chip card (1).

[Fortsetzung auf der nächsten Seite]

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Diese Aufgabe wird erfindungsgemäß durch eine Displaychipkarte mit den Merkmalen des unabhängigen Anspruchs 1 gelöst. In den abhängigen Ansprüchen sind vorteilhafte Ausgestaltungen der Erfindung angegeben.

- 5 Dementsprechend wird auch der Chipkartenprozessor in das Display verlegt, indem die Funktionen des Displaycontrollers und des Chipkartenprozessors in einem einzigen Chip, d.h. auf einem gemeinsamen Halbleitersubstrat, realisiert werden und dieser Chip auf demselben Substrat aufgebracht wird, auf dem auch das Display bzw. die Kontaktierungsebene des Displays
10 aufgerichtet ist.

- Dadurch entfallen die Verbindungsleitungen und aufwendigen Kontaktierungen zwischen dem Displaycontrollerchip und dem Prozessorchip, der in Chipkarten bisher immer unter dem Kontaktfeld der Chipkarte liegt. Mit der
15 Erfindung wird erreicht, daß lediglich noch fünf Anschlußleitungen zum Kontaktfeld der Karte notwendig sind. Aufgrund der großzügig dimensionierten, präzise platzierten Kontaktfeldbereiche kann diese Kontaktierung ohne viel Aufwand hergestellt werden.

- 20 Weitere Vorteile der Erfindung bestehen darin, daß nur ein Chip anstelle von zwei separaten Chips hergestellt werden müssen. Insbesondere bilden der Chip und das Display eine mechanische Einheit, die als ein einziges kompaktes Bauteil in die Karte integriert wird. Der Einbau eines separaten Prozessorchips entfällt somit.

25

Gemäß einer vorteilhaften Ausgestaltung der Erfindung erstreckt sich das Displaysubstrat bis unter das Kontaktfeld der Chipkarte, um den Kontaktierungsaufwand möglichst gering zu halten. Zu diesem Zweck liegt das Displaysubstrat mit dem Chipkartenprozessor oder zumindest mit zu dem

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Chipkartenprozessor führenden Kontaktanschlüssen unter dem Kontaktfeld der Chipkarte.

Nachfolgend wird die Erfindung beispielhaft anhand der begleitenden
5 Zeichnungen erläutert. Darin zeigen:

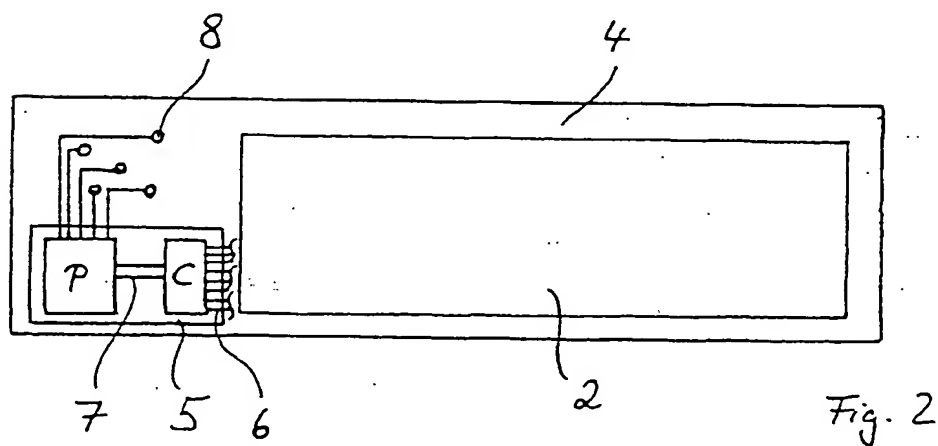
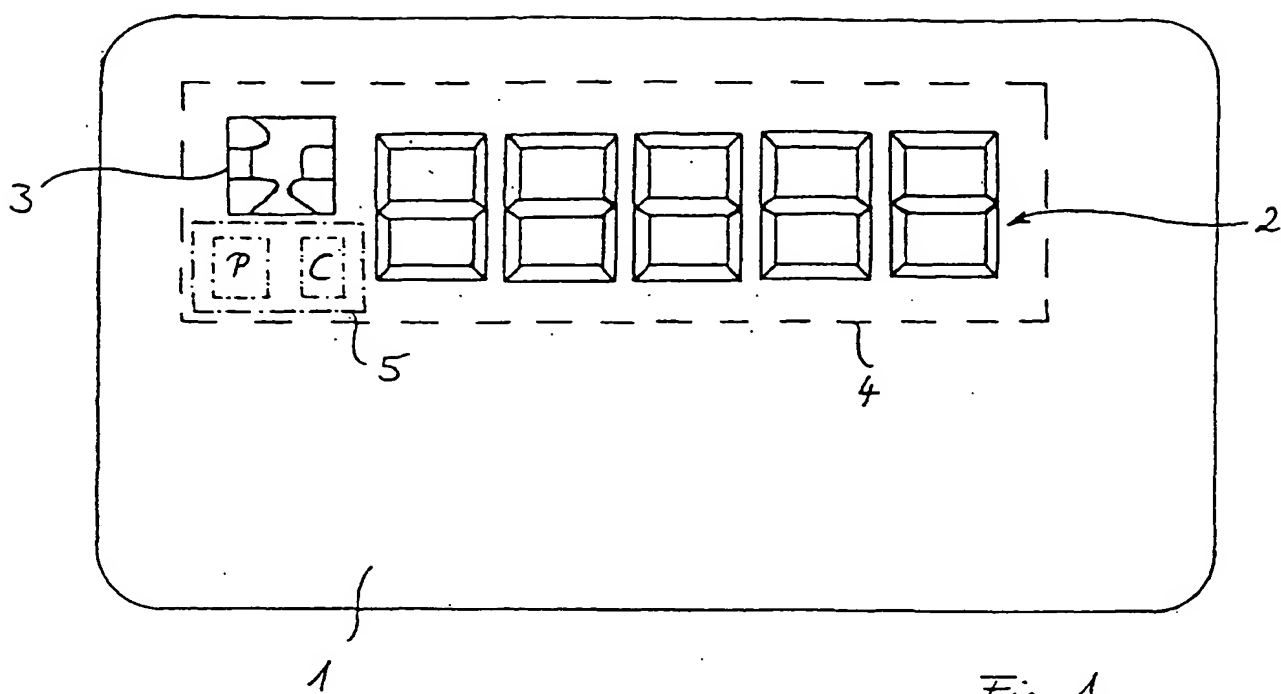
- Fig. 1 eine Displaykarte in Draufsicht mit in die Karte integriertem Displaysubstrat; und
- 10 Fig. 2 das Displaysubstrat der Karte nach Fig. 1 mit darauf angeordneten Bauelementen.

Fig. 1 zeigt eine Chipkarte 1 mit einem Display 2 und einem Kontaktfeld 3 für die kontaktbehaftete Datenübertragung zwischen der Chipkarte 1 und
15 einem Datenverarbeitungsgerät. Bei der Chipkarte handelt es sich beispielsweise um eine Karte mit Geldbörsenfunktion. Es können weitere Elemente in die Karte integriert sein, insbesondere eine Tastatur oder zumindest ein Schalter zur Aktivierung des Displays, sowie ein Unterschriftstreifen, Magnetstreifen, Hochprägungen und dergleichen.

20

Das Display 2 ist in dem dargestellten Ausführungsbeispiel als fünfstelliges 7-Segment-Display ausgeführt. Grundsätzlich ist die Erfindung aber im Zusammenhang mit jeder Art von Display geeignet.

25 In Fig. 1 sind die im Karteninneren liegenden, erfindungsrelevanten Bauteile strichliniert dargestellt. Dabei handelt es sich zum einen um ein Displaysubstrat 4, auf dem die Strukturen und Schichten des Displays 2 aufgebaut sind und zum anderen um ein auf dem Displaysubstrat 4 appliziertes Halbleiter-substrat 5 mit einem Chipkartenprozessor P und einem Displaycontroller C.



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According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHEDMinimum documentation searched (classification system followed by classification symbols)
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C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	DE 199 63 165 C (GIESECKE & DEVRIENT GMBH) 8 March 2001 (2001-03-08) column 4, line 1 - line 17 ----	1-3
A	DE 199 33 757 A (GIESECKE & DEVRIENT GMBH) 25 January 2001 (2001-01-25) column 2, line 43 -column 3, line 16; figure 2 ----	1
A	US 5 742 075 A (GRUBER CARL ET AL) 21 April 1998 (1998-04-21) column 12 -column 13; figures 8,9 ----	1
A	US 5 081 520 A (KOWA MIKA ET AL) 14 January 1992 (1992-01-14) column 5, line 58 - line 61; figure 16 ----- -/--	1

☒ Further documents are listed in the continuation of box C.☒ Patent family members are listed in annex.

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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	LEMME H: "ULTRAFLACHE DISPLAYS FUER CHIPKARTEN KUNSTSTOFFSUBSTRATE UND FERROELEKTRISCHE FLUESSIGKRISTALLE" ELEKTRONIK, FRANZIS VERLAG GMBH. MUNCHEN, DE, vol. 48, no. 20, 5 October 1999 (1999-10-05), pages 68,73-75, XP000927621 ISSN: 0013-5658 page 75, paragraph 2 -----	1
A	DE 199 29 912 A (ORGA KARTENSYSTEME GMBH) 18 January 2001 (2001-01-18) figures 1,2 -----	3

Information on patent family members

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Patent document cited in search report	Publication date	Patent family member(s)	Publication date
DE 19963165 C	08-03-2001	DE 19963165 C1	08-03-2001
		AU 2835501 A	14-05-2001
		WO 0133487 A2	10-05-2001
		EP 1230617 A2	14-08-2002
DE 19933757 A	25-01-2001	DE 19933757 A1	25-01-2001
US 5742075 A	21-04-1998	US 6017794 A	25-01-2000
US 5081520 A	14-01-1992	JP 4010447 A	14-01-1992
		JP 2302050 A	14-12-1990
DE 19929912 A	18-01-2001	DE 19929912 A1	18-01-2001
		AU 6425300 A	31-01-2001
		WO 0101341 A1	04-01-2001
		EP 1105838 A1	13-06-2001

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Angaben zu Veröffentlichungen, die zur selben Patentfamilie gehören

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DE 19963165	C	08-03-2001	DE	19963165 C1	08-03-2001			
			AU	2835501 A	14-05-2001			
			WO	0133487 A2	10-05-2001			
			EP	1230617 A2	14-08-2002			
DE 19933757	A	25-01-2001	DE	19933757 A1	25-01-2001			
US 5742075	A	21-04-1998	US	6017794 A	25-01-2000			
US 5081520	A	14-01-1992	JP	4010447 A	14-01-1992			
			JP	2302050 A	14-12-1990			
DE 19929912	A	18-01-2001	DE	19929912 A1	18-01-2001			
			AU	6425300 A	31-01-2001			
			WO	0101341 A1	04-01-2001			
			EP	1105838 A1	13-06-2001			



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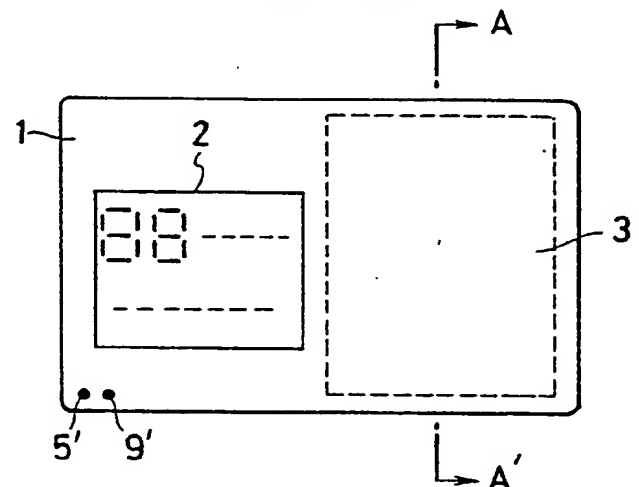
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(54) **Ferroelectric liquid crystal data storage card.**

(57) A card with a display function and a data storage capability comprises a pair of substrates; a plurality of electrodes on the substrates; a liquid crystal orientation control film on the electrodes; and a liquid crystal material with a strong dielectric property filled between the pair of substrates; wherein the liquid crystal material with the strong dielectric property is utilized to provide for a liquid crystal display region (2) and a data storage region (3) to write in, erase, and rewrite external data by addition of external energy to the liquid crystal material.

FIG. 1(A)



EP 0 291 259 A1

FERROELECTRIC LIQUID CRYSTAL DATA STORAGE CARD

FIELD OF THE INVENTION

The present invention relates to a card which has data storage capability in the card itself and can be used as a bank CD (cash dispenser) card, for example, as an ID card for maintaining secrecy, and as a card for presale of train passenger tickets and the like.

BACKGROUND OF THE INVENTION

Conventionally, the type of cards used as bank CD cards, cards for presale of train passenger tickets, and the like, are constructed by the application of magnetic tape to a paper or plastic plate. This magnetic tape section has the capability of storing account numbers, cash balances, and the like. However, the storage volume of the magnetic tape is extremely small and there is just enough room to store a few columns of numbers and symbols.

In addition, the details stored on this tape cannot be rewritten at will.

It is the so-called IC card that is a card having an integrated circuit to store electrically rewritable ROM (EEPROM) so as to overcome the problems as mentioned above.

Specifically, the IC card was developed with the object of increasing the storage volume, and consideration was given to the use of an integrated circuit providing a large memory volume to make rewrite possible. However, use of terminal equipment connected to a host computer is required not only for the total memory possible within such a card but also for even the smallest amount of data which the cardholder wishes to know, and the data must first be displayed on the display device of the terminal equipment to read the details.

For this reason, there has been a demand for a card which has the capability of displaying the stored data content so that it can be made to appear on the card itself.

For this reason, it is necessary that the part which performs the display and the part that stores the data be formed on the same card.

A liquid crystal display is most generally used as the part for performing the display because of its display capabilities and low electric power consumption and the like.

On the other hand, an EEPROM (Electrically Erasable Programmable ROM) is generally used as

the part which stores the data, because of its large storage content and the like.

However, forming them on the same card would increase the number of processes to cause the card structure to become complicated, and many other technical problems would be produced.

The inventor of the present invention has invented "an optical disk device using ferroelectric liquid crystals" for which application has been made for a patent (Japanese Patent Application Sho-60-130190). This is a storage (memory) using ferroelectric liquid crystals. The reading, rewriting, and erasure of the stored data is handled in the same way.

When an optical disk memory using the ferroelectric liquid crystals is applied in practice, there are a number of problems which must be overcome.

Examples of such problems are as follows.

(1) Between the disks, the molecules must be correctly lined up and a monodomain formed in a region corresponding to at least one bit.

(2) A storage region must be established on the disk. Specifically, adjacent individual storage regions must be properly isolated, and any case of erroneous access must be detected.

SUMMARY OF THE INVENTION

An object of the present invention is to provide, with due consideration to the drawbacks of such conventional devices, an erasable non-volatile card (IC card).

It is another object of the present invention to provide an IC card in which the display region and the memory region are formed from the same liquid crystals with strong dielectric characteristics with the number of processes curtailed.

It is another object of the present invention to provide an IC card whereby the above two points have been improved and the structure has been simplified.

It is another object of the present invention to provide an IC card wherein provision for the tracking of the card's storage region and elimination of orientation defects in the liquid crystals can be easily handled.

According to one aspect of the invention, this is accomplished in the present invention by the provision of a card with a display function and a data storage capability comprising a pair of substrates; a plurality of electrodes on the substrates; a liquid crystal orientation section on the electrodes; and a

liquid crystal material with a strong dielectric property filled between the pair of substrates; wherein the liquid crystal material with the strong dielectric property is utilized to provide for a liquid crystal display region to assume one of a matrix structure and a shape including diagrams, characters and symbols to display a shape including diagrams, characters and symbols, and a data storage region to write in, erase, and rewrite external data as a signal by addition of external energy including electrical, heat and light energies to the liquid crystal material.

Provided in one feature of the card of the present invention is a protruding section by which a storage region within the pair of substrates is partitioned at bit pitch intervals.

BRIEF DESCRIPTION OF THE DRAWINGS

These and other objects, features, and advantages of the present invention will become more apparent from the following description of the preferred embodiments taken in conjunction with the accompanying drawings, in which :

FIG.1(A) shows a plan view of a card according to the present invention.

FIG.1(B) shows a sectional diagram of the card according to the present invention.

FIG.1(C) shows a modification of the structure illustrated in Fig.1(B).

FIG.2 is a diagram of the states assumed by the liquid crystal element.

FIG.3 is a diagram showing a card system of the present invention.

FIG.4 is a graph showing the characteristics of transmitted light of the storage region of the card of the present invention.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Now referring to FIG.1 (A), a card is shown which has a display function and a data storage capability. FIG.1 (B) is a sectional drawing viewing in the direction A-A'. FIG.3 shows a system used by the card of the present invention.

In the storage method of the card with display function and data storage capability, as shown in FIG.2, a liquid crystal molecule 12 with an angle θ of its director is in a first inclined state with respect to the layer normal, and when external electric, thermal, or light energy is imparted to the molecule 12, it is changed to a second state at an angle $-\theta$ with respect to the layer normal.

At this time, the difference in the two states is determined from an optically developed change detected according to the difference between the first and second states, using an external detection means.

Specifically, the data is recorded in memory as a signal with the first state represented as 0 and the second state as 1.

The method by which the liquid crystal molecule is made to assume the first and second states is now explained by means of the sectional diagram of the memory region part of this card, shown in FIG.1(B).

It is made sure by the following method that the directions of the molecules are directed in the same direction throughout the thickness of the liquid crystal layer between a pair of substrates. In the method, which is generally referred to as SSFLC, the states are obtained by providing a sufficiently ace between the pair of top and bottom substrates 4 and 10 than a spiral pitch of the ferroelectric liquid crystals 7. In this method, by means of an imposed force from the contact surface of the substrate, the liquid crystal molecule assumes the two states.

The stability of the states are expedited by utilizing the difference in polarity produced when providing a ferroelectric film used for a pair of top and bottom liquid crystal orientation films 6 and 8, so that the liquid crystal molecules are directed.

In addition, there is another expediting method in which the liquid crystal orientation films 6 and 8 are laminated from a dielectric film and a metallic film, and an electric field generated by storing an electrical load in the metallic film, is applied to a liquid crystal layer 7 so as to produce the first and second states.

The present invention will now be further explained by means of the following embodiment.

A memory card has a pair of opposing substrates 4 and 10, which is 8.6 cm wide and 5.4 cm long. Also, on the inside of the pair of substrates 4 and 10, there is a pair of transparent electrodes 5 and 9. On this pair of electrodes 5 and 9, a pair of orientation control films 6 and 8 is respectively formed. Between the substrates 4 and 10, an FLC 7 is filled.

In addition, at least one protruding section 23, by which the storage region is partitioned at bit pitch intervals, may be provided for position marker on the liquid crystal orientation control film 8, if necessary, as illustrated in fig.1(C).

On the drawing, the interval is shown as being very wide, being enlarged for convenience. Actually, when utilized, one protruding section 23 is provided every few micrometers and the storage region on the card is partitioned in one direction, parallel to the periphery of the card.

Specifically, a plurality of protruding sections 23 with a height of 1.5 μm , width of 3 μm , at intervals of 5 μm are formed on the orientation control film 8 from a photosensitive polyimide resin. At this time, after the photosensitive polyimide resin is applied to the front surface, the specified pattern is formed by means of a photomask.

Also, the FLC 7 is filled into the space between the substrates.

A sealant 11 is applied around the periphery of the memory card to prevent the FLC 7 from contacting the air. A pair of external contact electrodes 5' and 9' are provided on the outside periphery of the memory card, extending from the pair of electrodes 5 and 9. The external contact electrodes 5' and 9' are connected with a lead terminal led out from a signal source 13, and perform erasure by the application of a prescribed voltage when erasing the writing (full surface erasure method) in the memory.

In this way, all zeroes are written in as data for the entire memory region. Specifically, light is emitted from a light beam, particularly infrared rays 19, via a mirror 18 and a half-mirror 17, and via a system 22 for optical focussing and position compensation and the like, onto a specified address in the liquid crystal layer out of domains which are arranged in matrix form across the whole surface, and the state of the liquid crystal molecules at the specified address are shifted from the initial state thereby performing a write-in. At this time, quick, accurate data write-in is possible by the application of a lower voltage than a threshold direct voltage to reverse the liquid crystals between the top and bottom electrodes 5 and 9. The light reaches a photosensor 14 via a mirror 16 and the write-in of the data is monitored.

To read out the data, a laser beam radiated from a semiconductor laser 21 is directed onto the memory card via a polarizer 20 and the half-mirror 17, and via an optical system 22 for focussing position compensation or auto tracking. The light transmitted onto the storage region 3 of the memory card reaches a light receiving sensor 14 via the polarizer 15 and the mirror 16. In the light sensor 14 the transmission or non-transmission of the laser beam is detected to read the data.

In this case, when the data is being written in and read out, the mirrors 16, 17 and 18 and the optical system 22 are moved relatively parallel to the card 1 and the data is written into, or read out of, the specified address.

In other words, the card 1 may be moved, or the mirror and optical system moved automatically, or both may be moved simultaneously.

When this data is written in or read out using the protruding section 23, the volume of the liquid crystal 7 on the protruding section 23 is smaller or

completely non-existent in comparison with the data storage section 26, so that in the case where the write-in and read-out beams are offset from the data storage section 26, an optical change is not obtained based on the optical anisotropy of the liquid crystal material when the beam reaches the photocell 14. Accordingly, the beam only occurs in a transparent or opaque state (according to the arrangement of the light polarizing means 15 and 20). This is detected electrically, and the position of the mirror and optical system is revised. It is then possible to direct the write-in and read-out beams correctly onto the data storage section 26.

In addition, even if the protruding section is formed from a reflecting material, the position modification can be performed in the same way.

In the storage region 3 of the present invention, the substrates 4 and 10 are formed from plastic or Corning 7059 glass. On these substrates, a transparent conductive film ITO (indium tin oxide) is formed by the vacuum deposition method. Then, on the inside of the pair of substrates 5 and 9, the asymmetric oriented films 6 and 8 are provided, and a liquid crystal cell is formed to have a substrate thickness of 2 μm . The liquid crystal layer 7 is interposed between the substrates. In order that orientation is carried out, on the electrode 5, PAN (polyacryl nitril) and PVA (polyvinyl alcohol) are provided by a spinning method to a thickness of 0.1 μm , and subjected to a common rubbing process. The orientation control film may be formed of SiO₂ or polyimide instead.

An oriented film of an inorganic compound is formed on the other electrode 9, and the liquid crystal orientation control film 8 is provided without rubbing.

Between the liquid crystal orientation control films 6 and 8, a ferroelectric liquid crystal, for example, biphenyl-related chiral smectic liquid crystals is disposed.

Other than these, BOBAMBC FLC and the like or a blend of a plurality of liquid crystals FLC can be filled. An example of the threshold value characteristics of FLC is given in FIG.4. Also, FIG.4 shows a pair of curves 24 and 25 obtained by the application of ± 5 V. Specifically, the light is transmitted or not transmitted by causing the liquid crystal molecule to reverse, and at the same time, hysteresis showing the memory effect is obtained.

In FIG.4, the vertical axis is the transmission ratio of the light.

The display region 2 of the card 1 has substantially the same structure as the previously described storage region 3 except that in this embodiment of the present invention, the pair of electrodes 5 and 9 have a matrix structure, so that characters, diagrams, and symbols can be displayed by means of dots. In addition, the required

IC is provided on the card.

Specifically, the storage region 3 and the display region 2 of the card differ only in the shape of the electrodes, with the other structural parts being exactly the same. Therefore, a card which has both of the display function and storage capability can be formed by a conventional process for preparing liquid crystal display devices.

In addition, a pair of light polarizer 15 and 20 is applied, completely covering both sides of the card, so that a light polarizing means in the optical read-out means becomes completely unnecessary.

Also, the light polarizer on the card do double duty as polarizing plates in the display and storage regions.

In addition, the liquid crystal molecule 12 has the capability of self-alignment along the protruding section 23. In this way, the liquid crystal molecules of the data storage section 26 are provided with an improved orientation status with no defects, and the data can be accurately stored in memory.

The protruding section 23 can also serve as a spacer for the liquid crystal cell. This allows a reduction in the steps and costs of the production process.

The above is one example of the use of this display memory card. In the case where it is used as a CD (Cash Dispenser) card by a bank or the like, the cardholder goes to the bank, then the electrodes 5 and 9 of the card are inserted into the CD terminal equipment connected to a computer, so that power is fed to the card, and data such as the cardholder's name, the account number, details of past transactions, and the like is extracted from the card memory by the optical system read-out means. At the same time, new data is written in by optical means, and the required data is displayed on the card.

The liquid crystals used in this display are liquid crystals which show ferroelectricity (chiral smectic C liquid crystals), which when sealingly enclosed in a parallel plane space narrower than the spiral pitch of the liquid crystals, is positioned in a stable two-axis direction. One of these two directions can be selected by means of an electric field. Also, even if this electric field is removed, the liquid crystals remain secured in a stable position. By combining with a polarizing plate, the display (of characters and the like) can be maintained.

Accordingly, when the card is removed from the CD terminal, the power supply to the card is cut off, but the data remains in the memory region based on the liquid crystals with strong dielectric properties and in the display region with strong dielectric properties.

In this way, the user at any time, anywhere, can see the contents of the memory.

This example describes the method of using

the card of the present invention as a bank CD card only, but it is also possible to apply the card to other uses.

In this embodiment, the card of the present invention is shown to comprise the display region 2 and the storage region 3 of a transmission type, but, in other embodiments, use of a reflection type does not diminish the effect of the present invention. In such a case, a laser beam is reflected in the electrode section of the card which is one part of the pair of electrodes for memory, so that it is possible to make one of the substrates from metal, whereby the card of the present invention can be made at a lower price.

In addition, in the case where an external terminal is not provided in the card of the present invention, it is possible to read the memory of the card without using an electrical connection, and in this case the storage section works as a ROM.

Also, it is possible to add two-color dye within the FLC to store and display data in guest-host mode. In this case, there is the feature that the polarized light means may be one or zero.

By incorporating in the interior of the card form a display device and a storage region, which use ferroelectric liquid crystals, as opposed to what has been available up to the present in cards with magnetic card systems, a memory function is provided in the card itself, and one part or all of the memory of this card can be checked at any time through a display device, for which a special terminal device is therefore unnecessary.

In addition, because power is necessary only for rewriting, it is not necessary to have a source of power in the card itself. If a power source is provided on the card, a solar cell may be used.

Also, because it is possible to provide a card with the storage region and the display region having substantially the same structure, when the card is prepared, a card with two functions can be created at a low cost using the same process as for conventional liquid crystal display devices, and a memory card which can easily be mass-produced is obtained.

The storage region of the present invention has substantially the same structure as the display region. However, no data in the storage region is externally displayed, and is known unless it is read out with dedicated equipment. Therefore the card can be used as an ID card. Also, a source of power such as batteries and the like is unnecessary, and control equipment such as a CPU and the like is not required. Therefore, the mechanism is simple and it is possible to produce a large volume at low cost. Also, the reliability of the stored data is improved by providing the data storage means and the display means. In addition, rewriting is impossible unless a voltage or heat is applied to the

liquid crystal, so the data cannot be erased by normal, everyday temperature changes and is therefore very stable, keeping the advantages that it is possible to reuse the card by revising or changing the stored data.

In addition, because a light polarizing means applied to the card can be commonly used for the display region and the storage region, lower cost production is possible.

Claims

1. A non-volatile ferroelectric crystal card memory comprising:

a pair of card substrates, at least one of said substrate being transparent;

a first ferroelectric liquid crystal layer disposed between said substrates; and

an electrode arrangement provided on the inside surfaces of said substrate for applying an electric field to change the anisotropic direction of said liquid crystal layer.

2. The memory of claim 1, wherein said electrode arrangement is a pair of electrodes extending opposite to each other with said liquid crystal layer inbetween.

3. The memory of claim 2, further comprising a display section which is composed of a second ferroelectric liquid crystal layer whose optical property is controlled by applying an electric field thereto.

4. The memory of claim 3, further comprising a second electrode arrangement for applying a particular portion of said second liquid crystal layer to display visual information.

5. The memory of claim 4, wherein said information is letters, characters, symbols, or other images.

6. The memory of claim 5, wherein said second electrode arrangement is formed into a matrix structure.

7. The memory of claim 5, wherein said first and second liquid crystal layers are made of a same ferroelectric liquid crystal material.

8. The memory of claim 7 further comprising an IC chip for driving said memory and said display.

9. The memory of claim 8 further comprising an external terminal for receiving control signal from an external device.

10. The memory of claim 8 further comprising an external terminal for receiving electric power required for operation of said memory.

11. The memory of claim 8 further comprising a solar cell for supplying an electricity to said memory.

12. The memory of claim 1 further comprising a pair of orientation control films provided on the opposed insides of said substrates, one film being made of polyimide and the other being made of SiO₂.

13. The memory of claim 1, wherein said memory substrate is 8.6 cm wide and 5.4 cm long.

14. A data storage device comprising a first liquid crystal structure having regions in which the state of the liquid crystal can be controlled to define stored data which can be read out by external equipment, and a second liquid crystal structure having regions in which the state of the liquid crystal can be controlled to define a display visible to a user.

15. A device in the form of a flat planar card having a non-volatile data storage region and a non-volatile data display region capable of effecting a display corresponding to at least some of the data stored in said data storage region, both said regions comprising liquid crystal material and said data storage region being addressable from the exterior of the device for at least reading data from said data storage region.

FIG. 1(A)

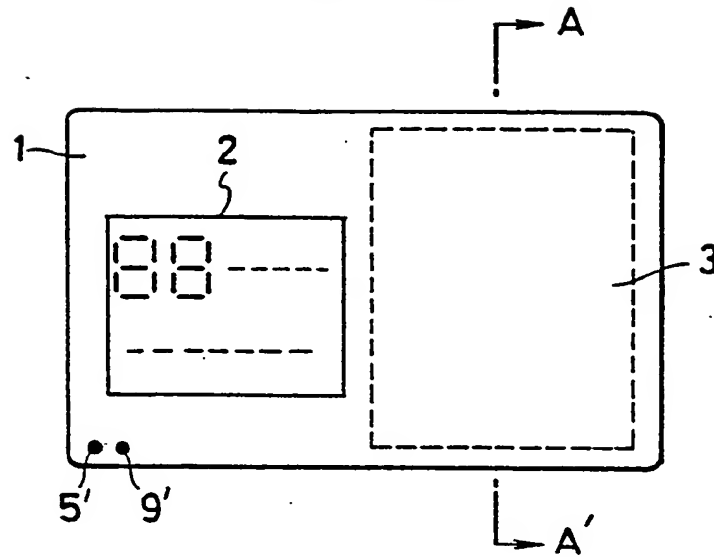


FIG. 1(B)

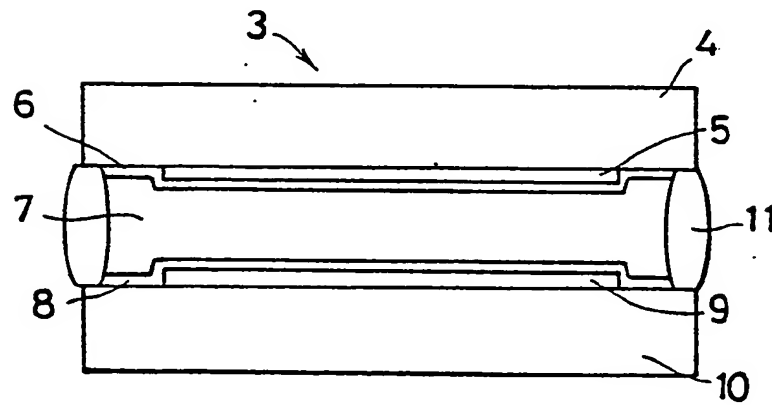


FIG. 1C

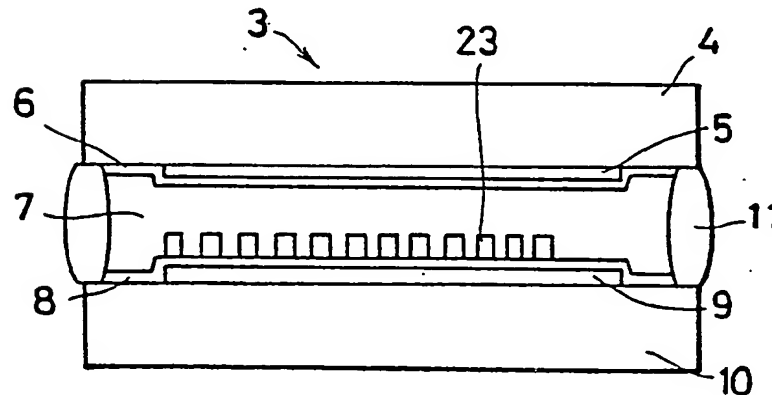


FIG.2

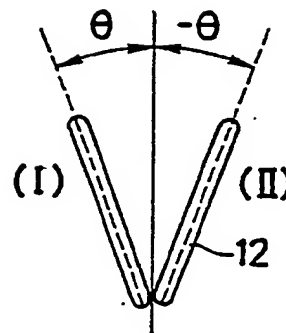
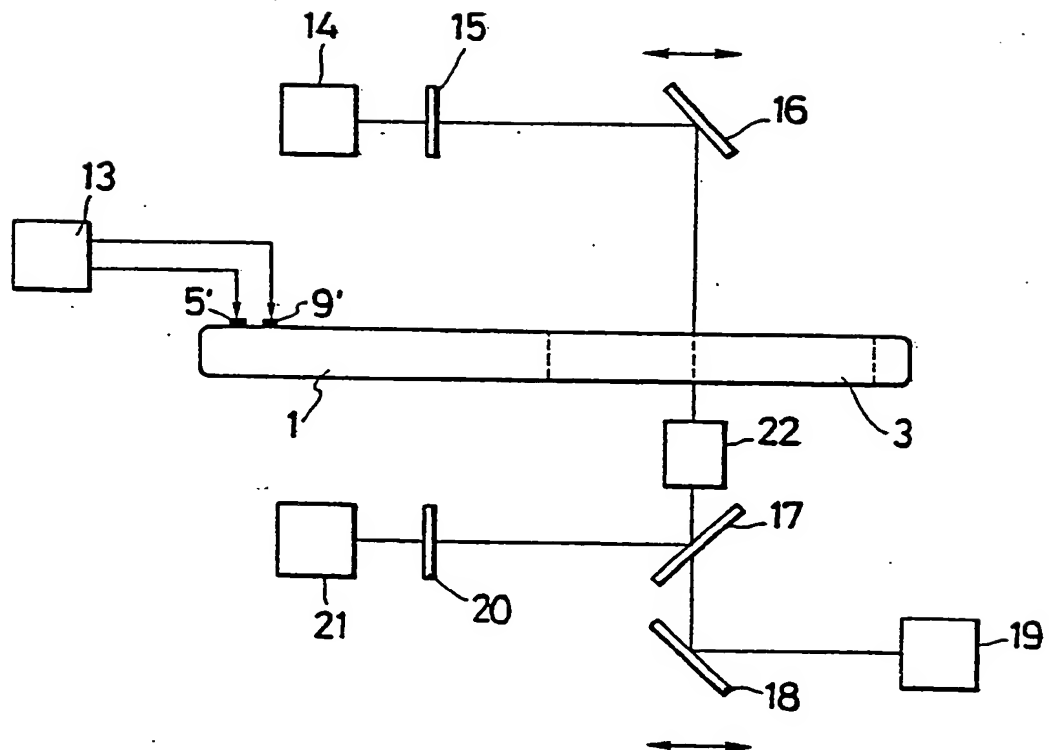
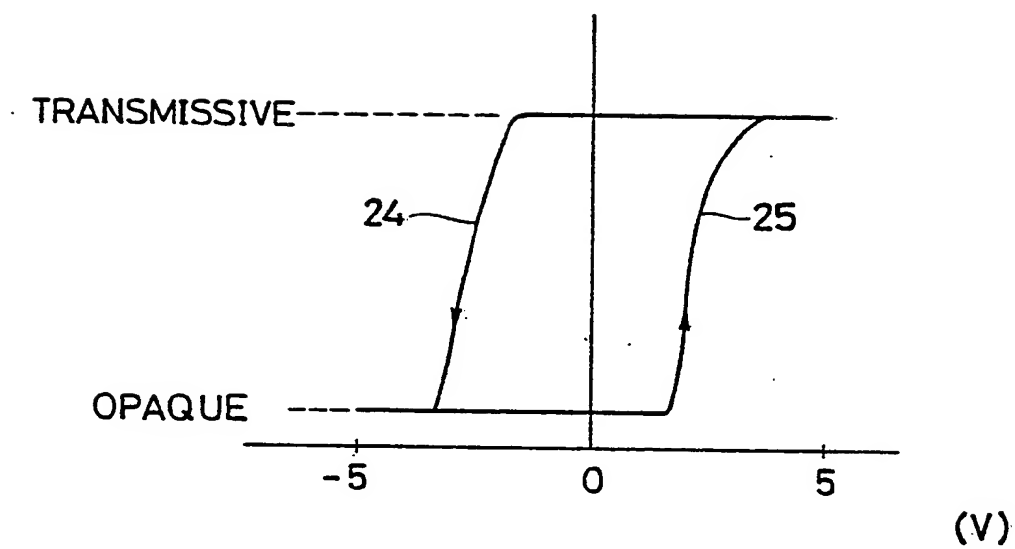


FIG.3



Neu eingereicht / Newly filed
Not yet examined / Not yet examined

FIG. 4



DOCUMENTS CONSIDERED TO BE RELEVANT			EP 88304184.0
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl. 4)
Y	GB - A - 2 177 228 (SEMICONDUCTOR ENERGY LABORATORY) * Totality *	1, 14, 15	G 07 F 7/10 G 09 F 9/35 G 02 F 1/137
A	--	2, 9, 10	
Y	EP - A2 - 0 220 703 (CASIO COMPUTER) * Fig. 1; abstract *	1, 14, 15	
A	--	3-6, 8-10	
A	GB - A - 2 178 582 (CANON) * Fig. 10; abstract *	3-10, 14, 15	
D	& JP-A-60-130 190 ----		
			TECHNICAL FIELDS SEARCHED (Int. Cl. 4)
			G 07 F G 09 F G 02 F G 09 G G 06 K
The present search report has been drawn up for all claims			
Place of search VIENNA		Date of completion of the search 08-08-1988	Examiner DRÖSCHER
CATEGORY OF CITED DOCUMENTS			
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	